

FIG. 1
RELATED ART

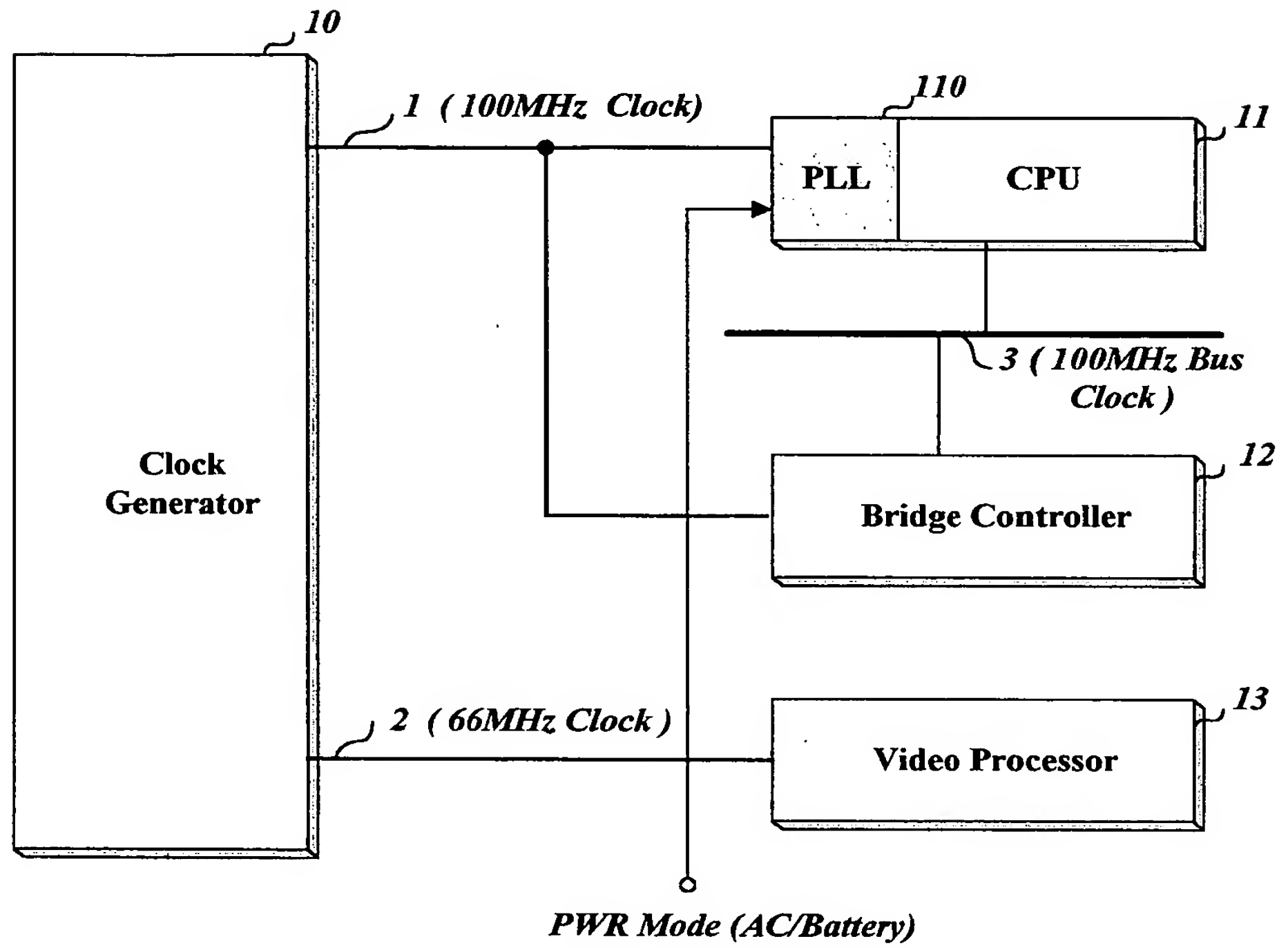


FIG. 2
RELATED ART

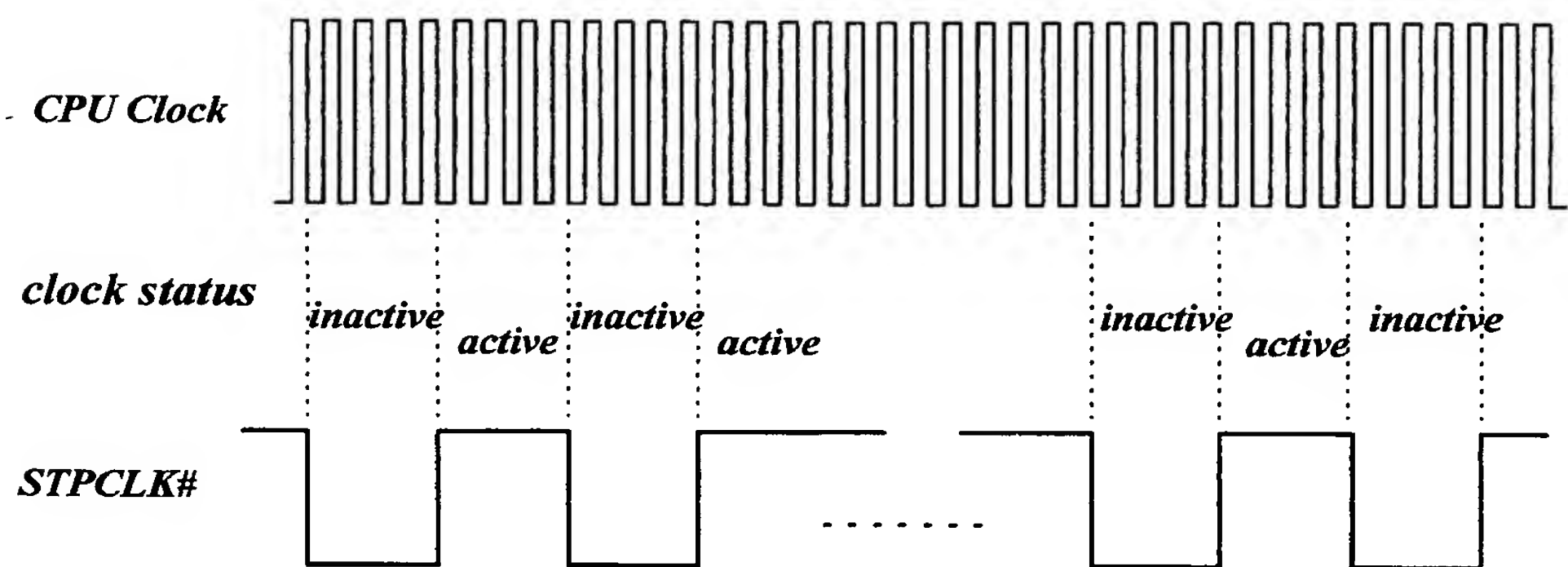


FIG. 3

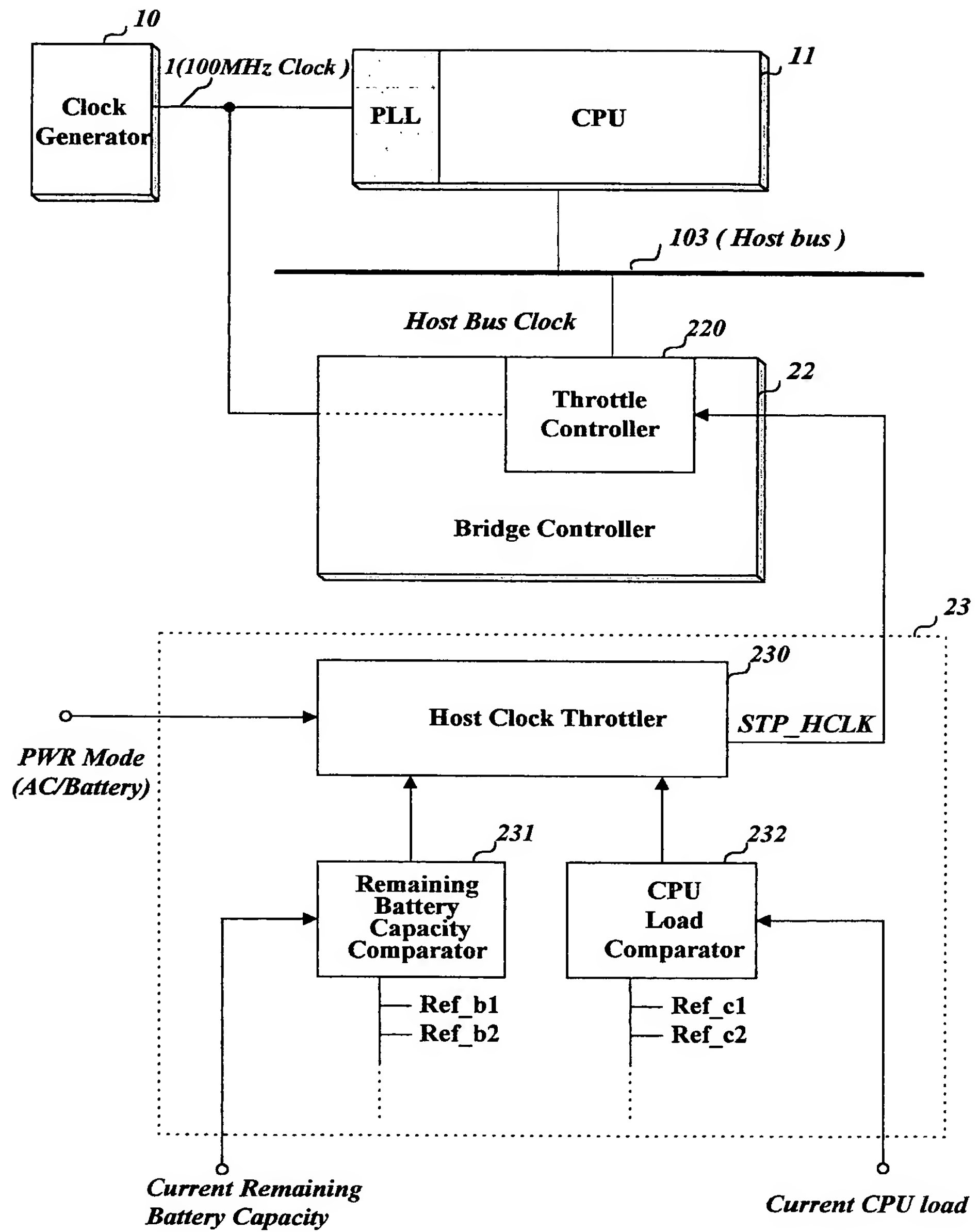


FIG. 4

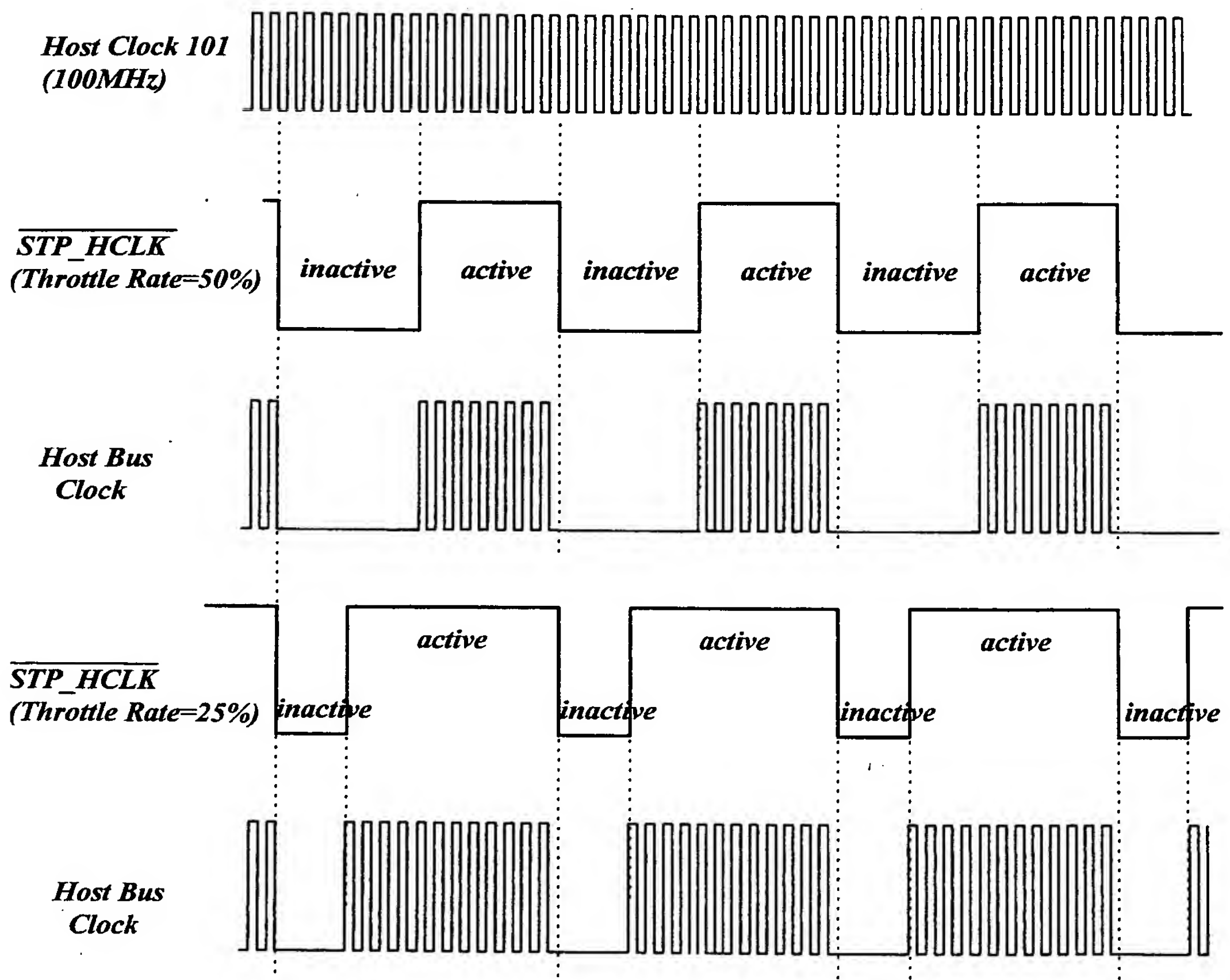


FIG. 5

1 (100 MHz Clock)

220

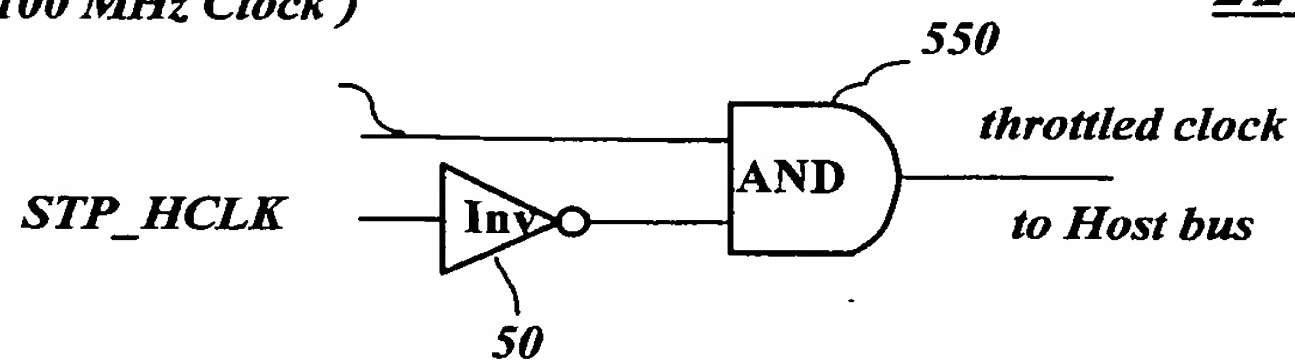


FIG. 6

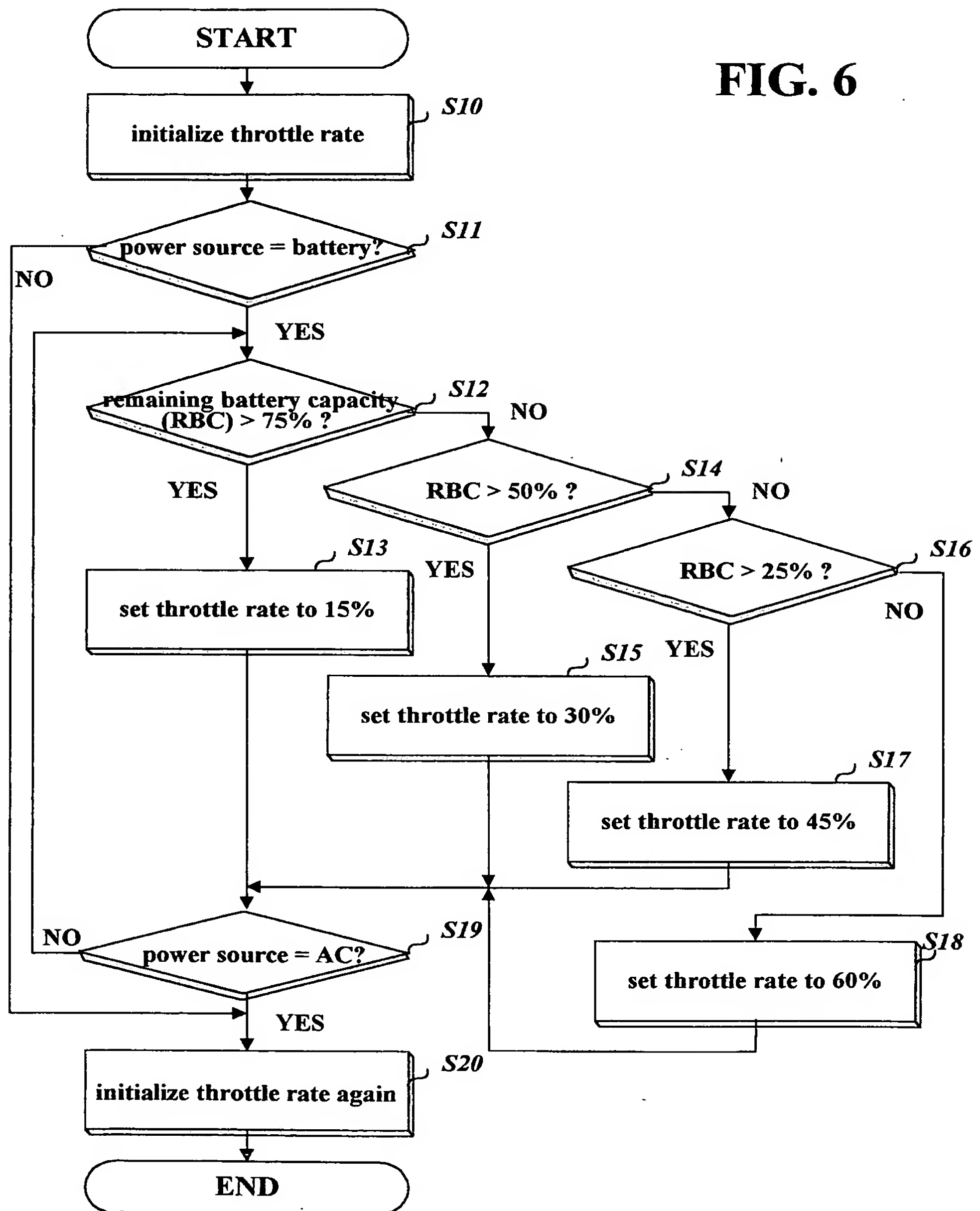


FIG. 7

Remaining Battery Capacity	System Performance	Throttle Rate of Bus Clock
100 %	100 %	0 %
75 %	85 %	15 %
50 %	75 %	30 %
25 %	55 %	45 %
0 %	40 %	60 %

